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FIG. 1 communication system

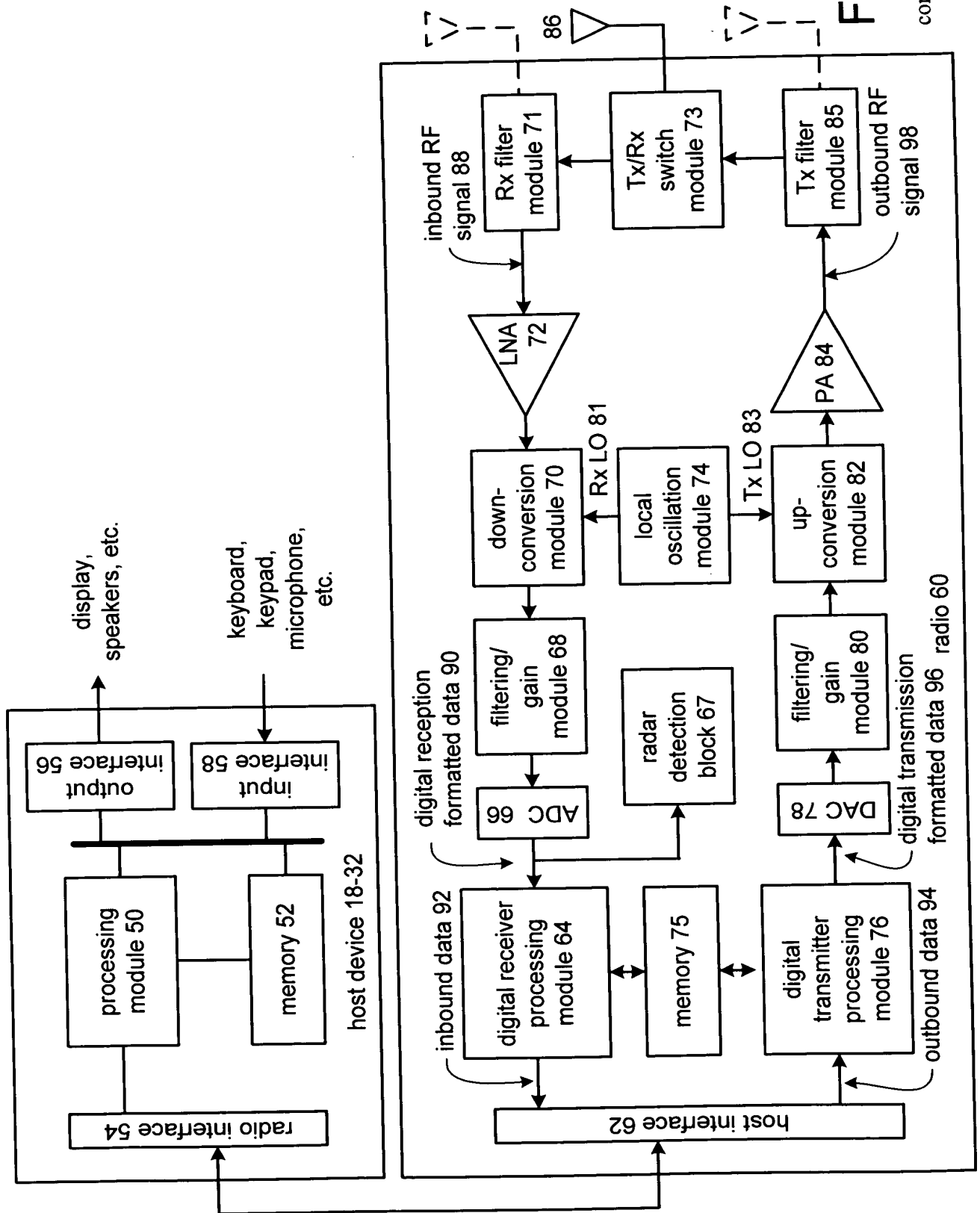
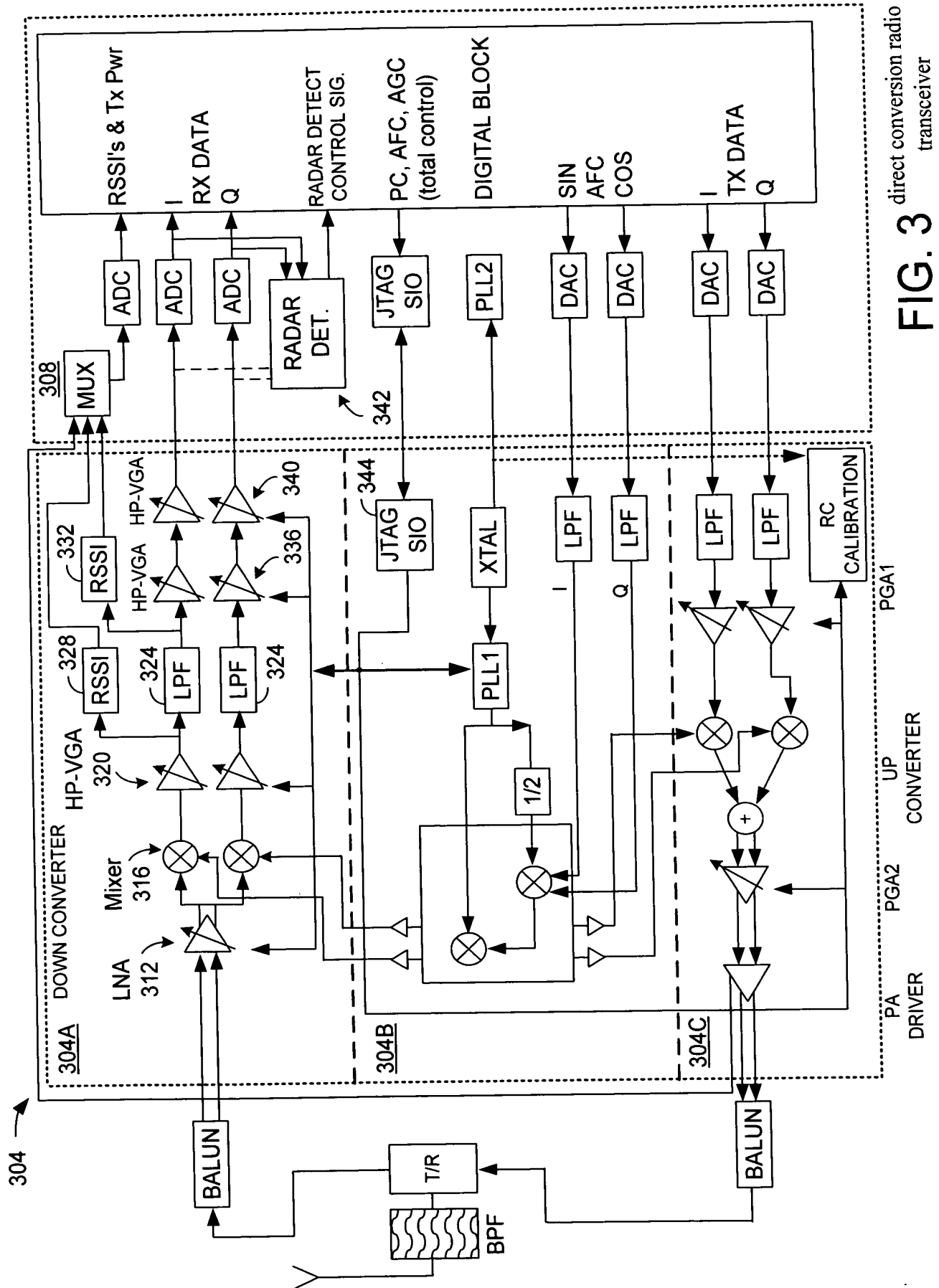


FIG. 2
wireless
communication
device



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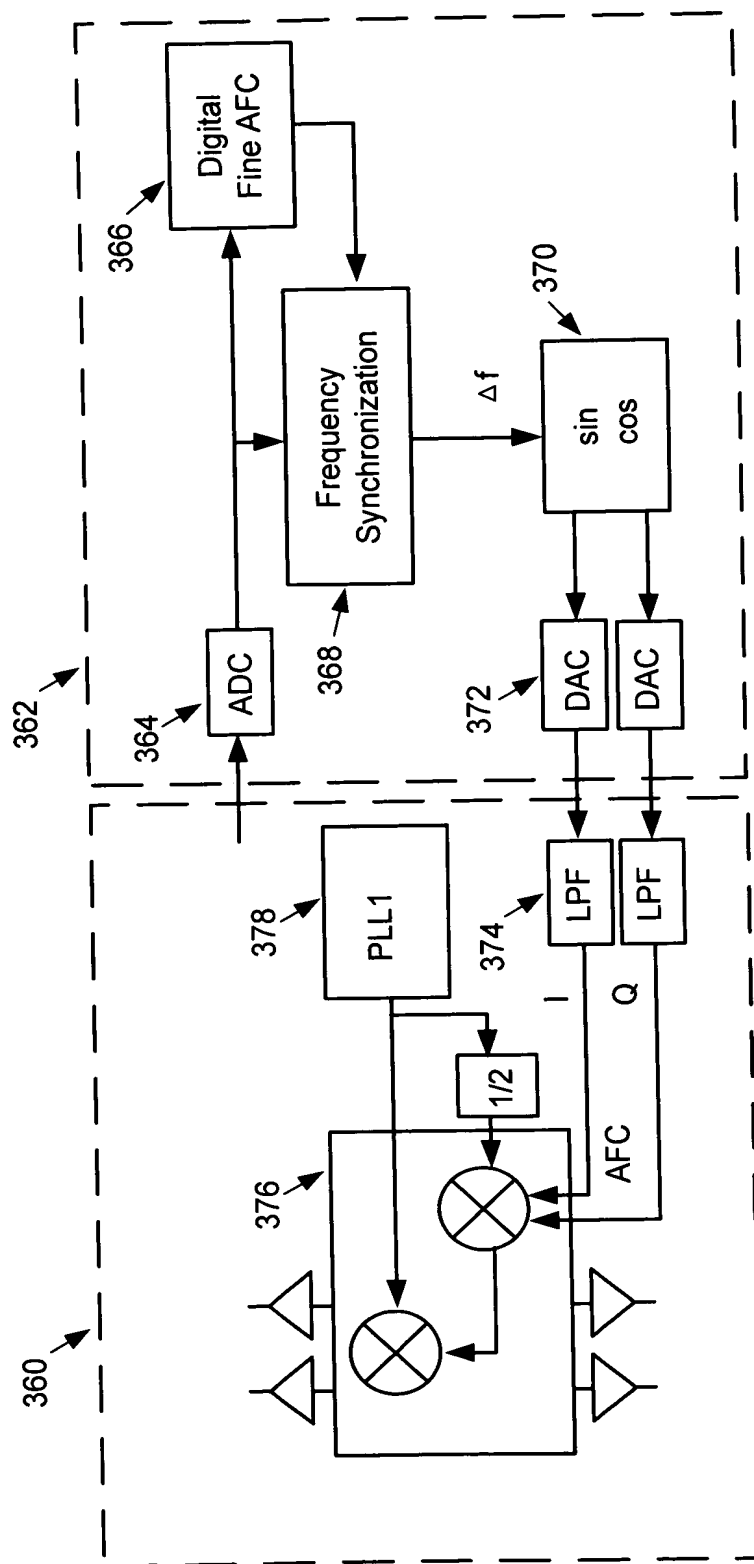


FIG. 4 receiver automatic frequency control circuit

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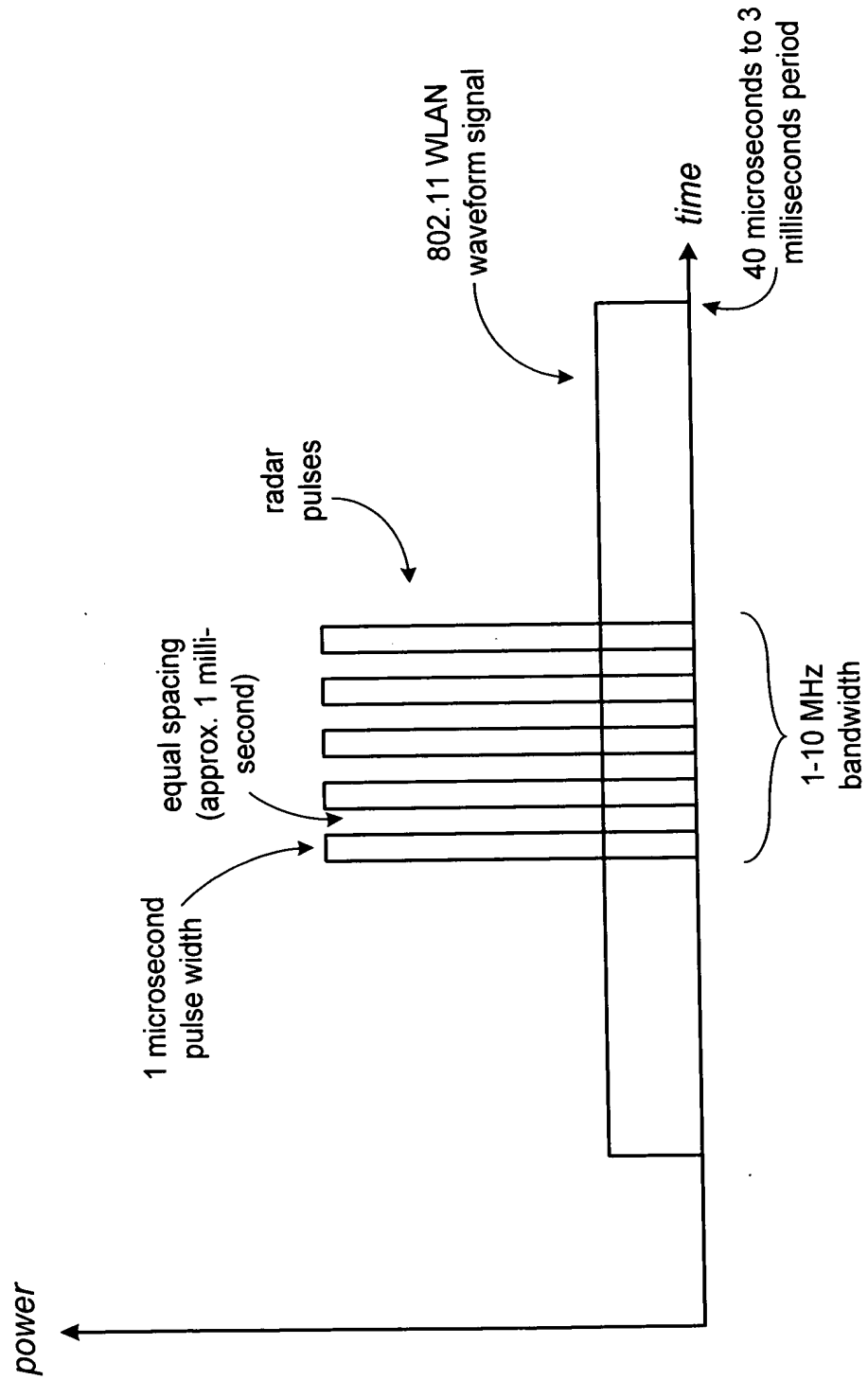


FIG. 5 radar and WLAN signals

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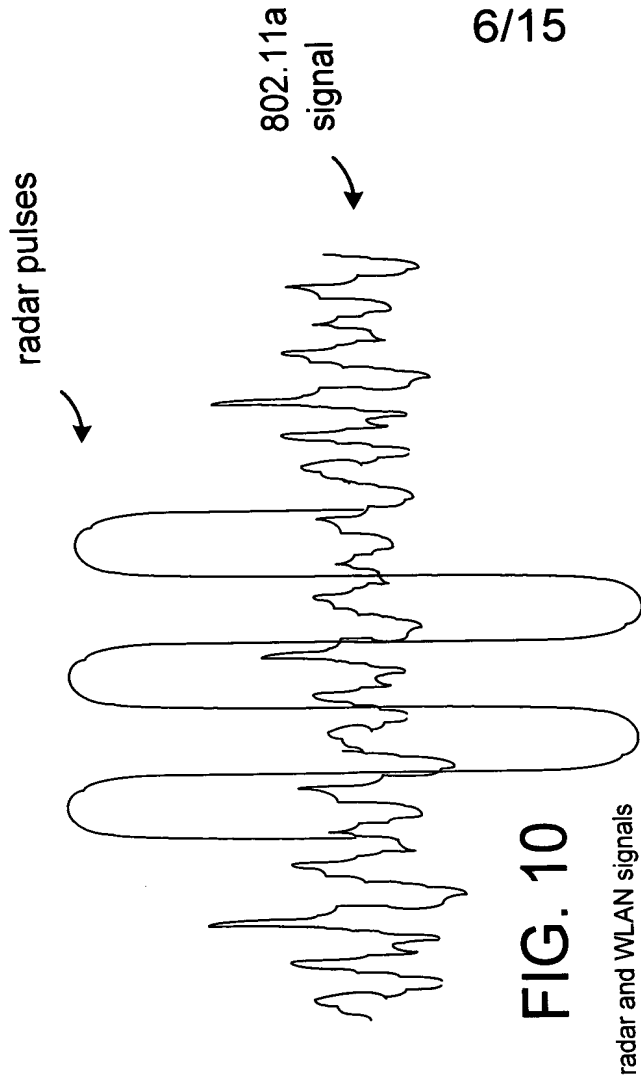


FIG. 10

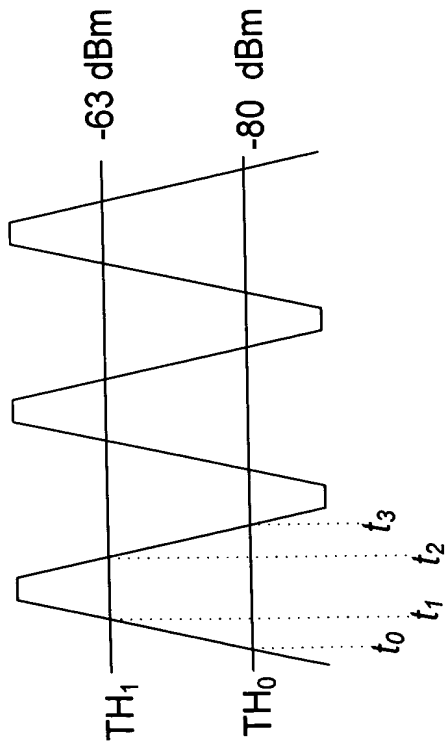


FIG. 7



FIG. 6

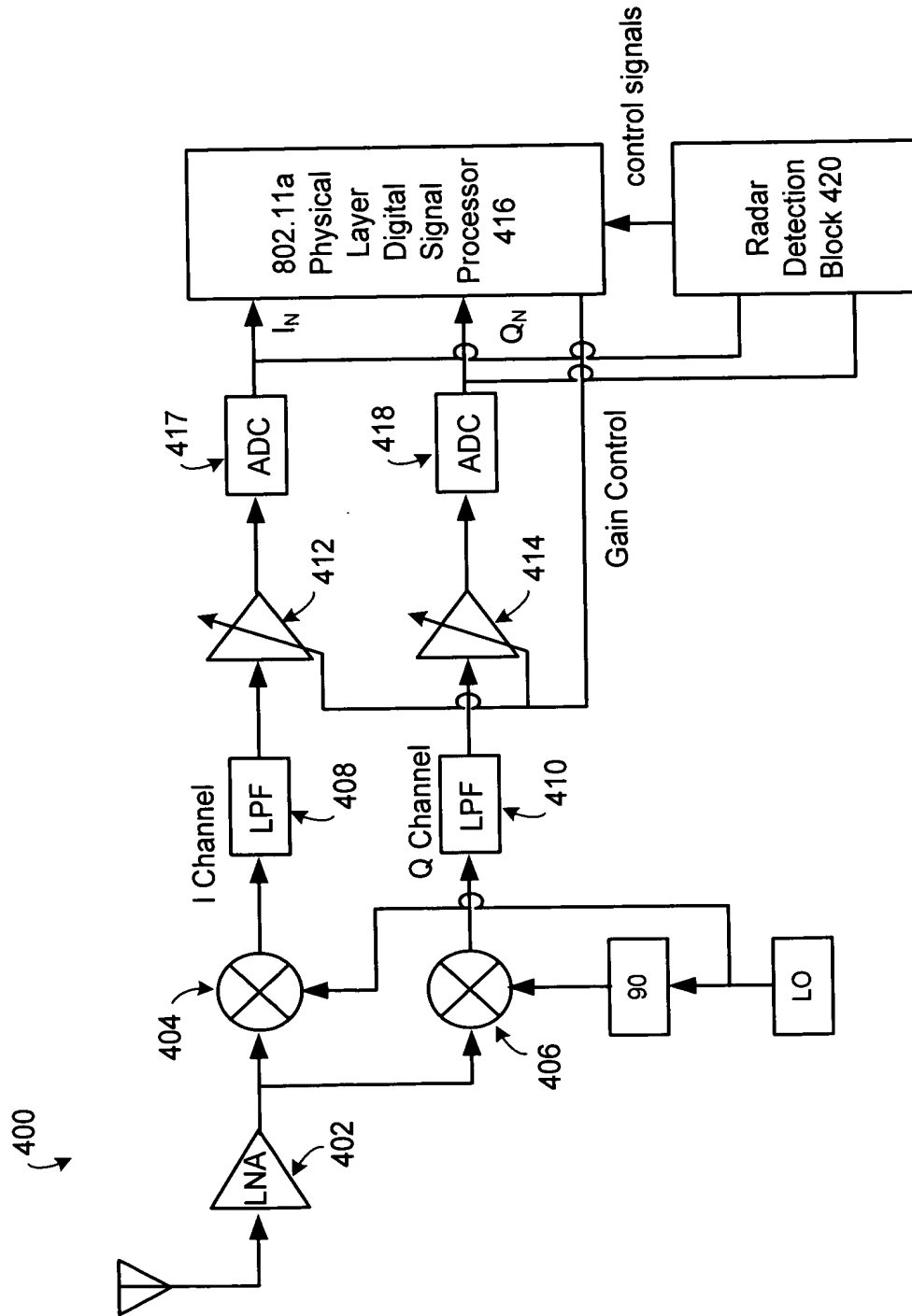
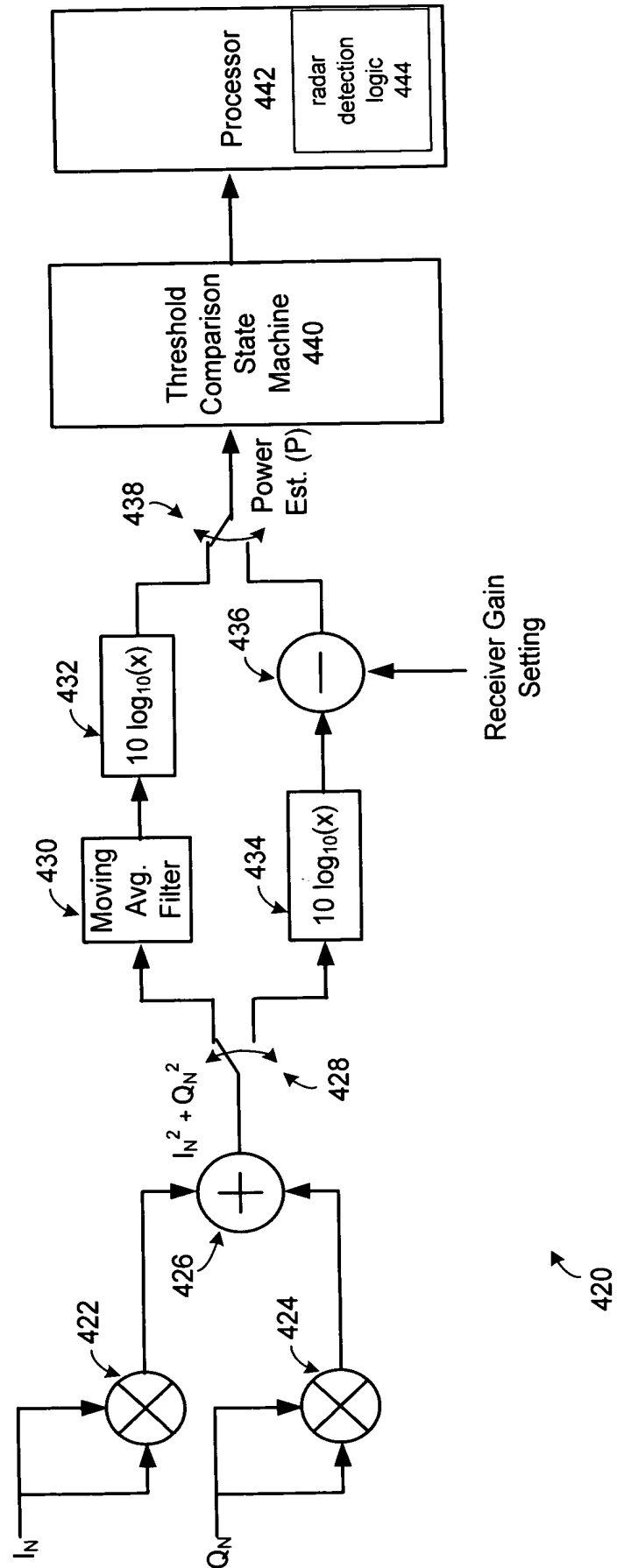


FIG. 8 receiver with radar detection block

To Processor



radar detection logic

FIG. 9

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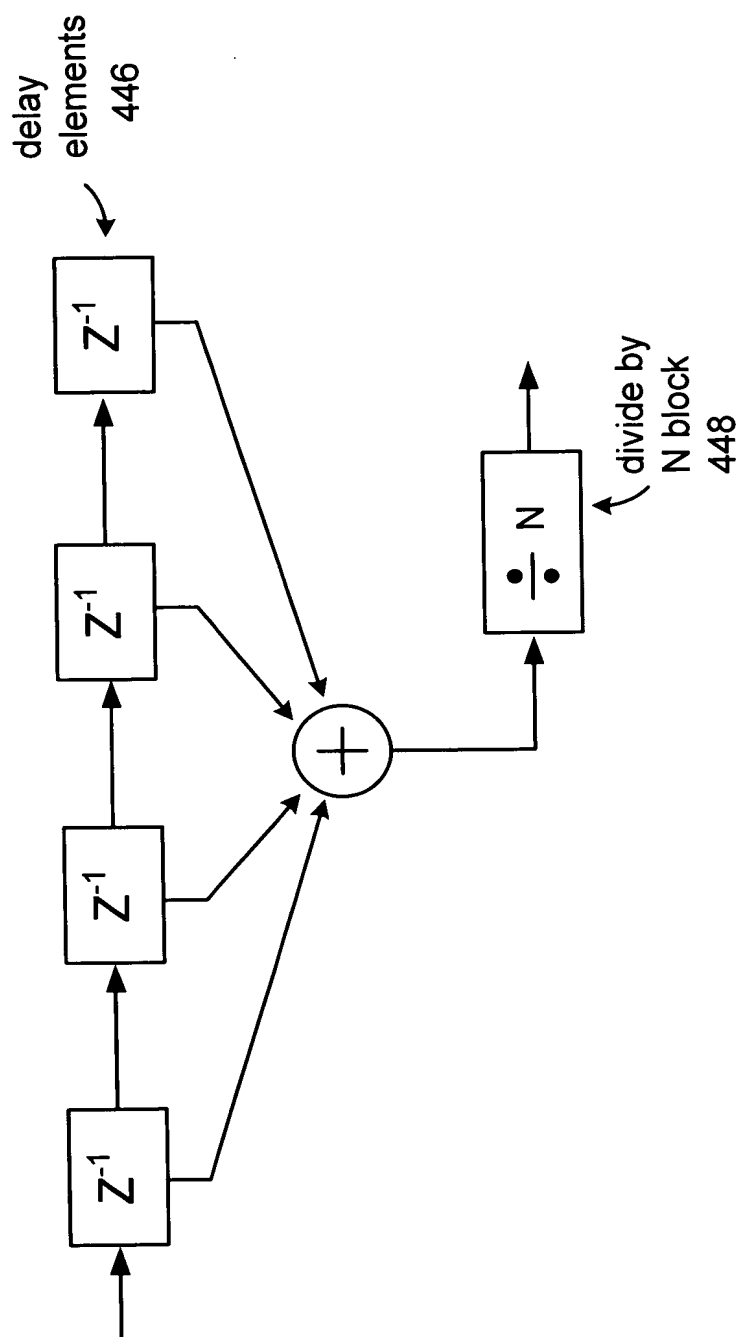


FIG. 11 moving average determination logic

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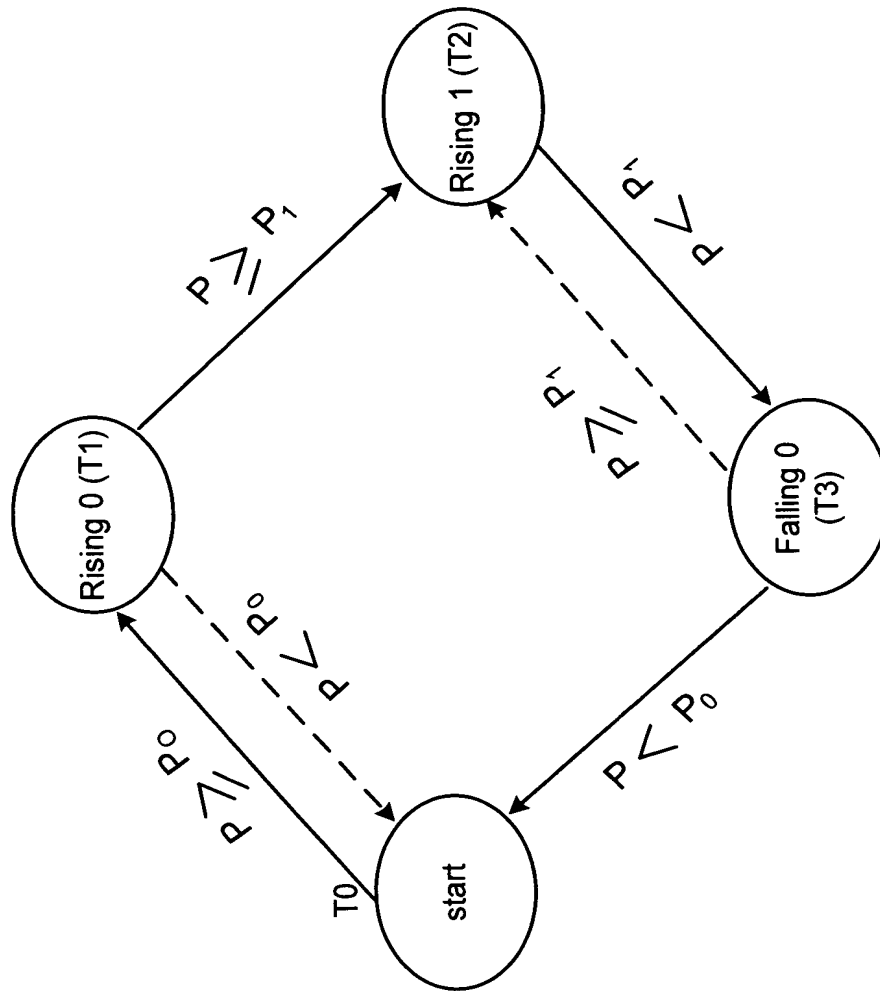


FIG. 12 state machine for detecting radar pulses

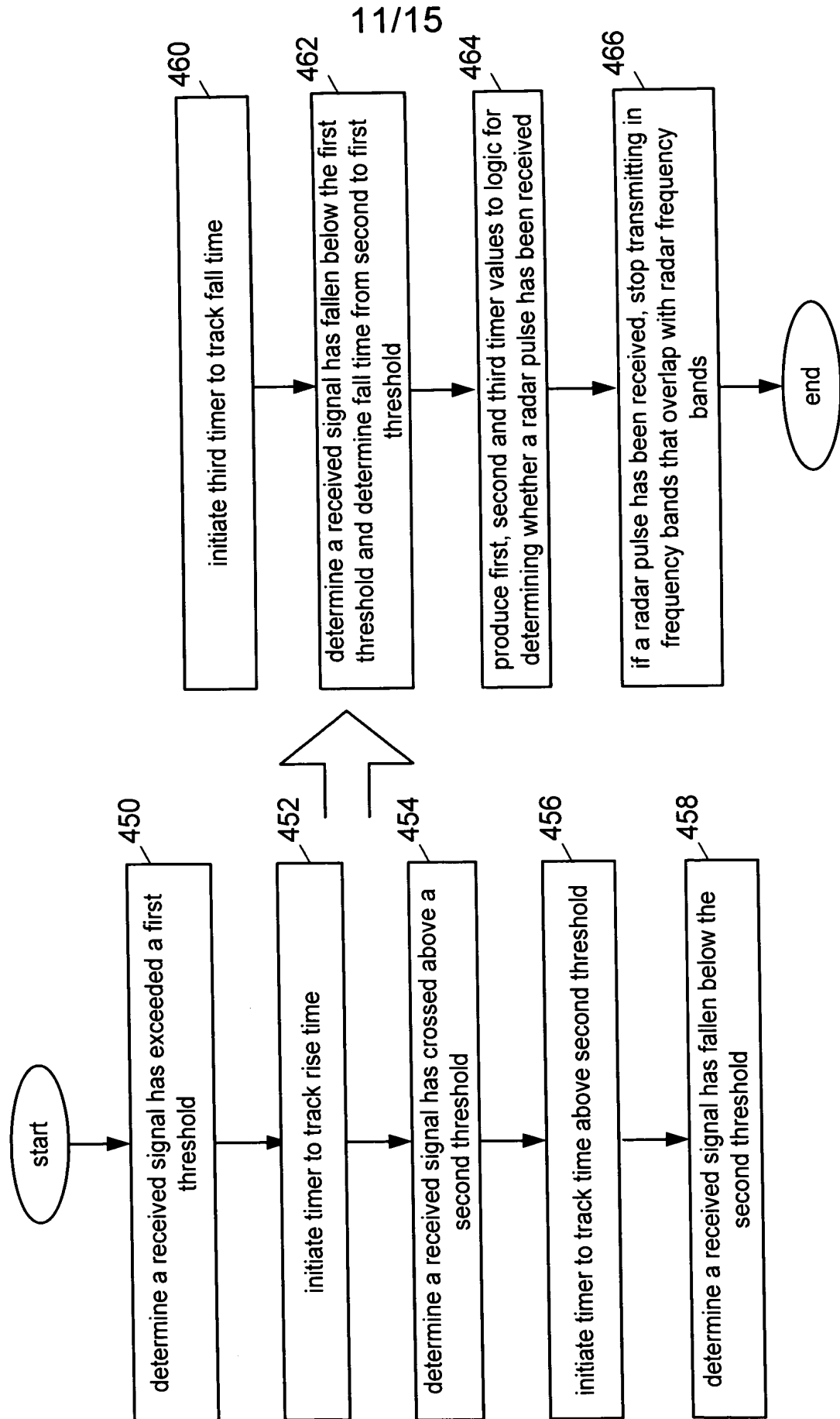
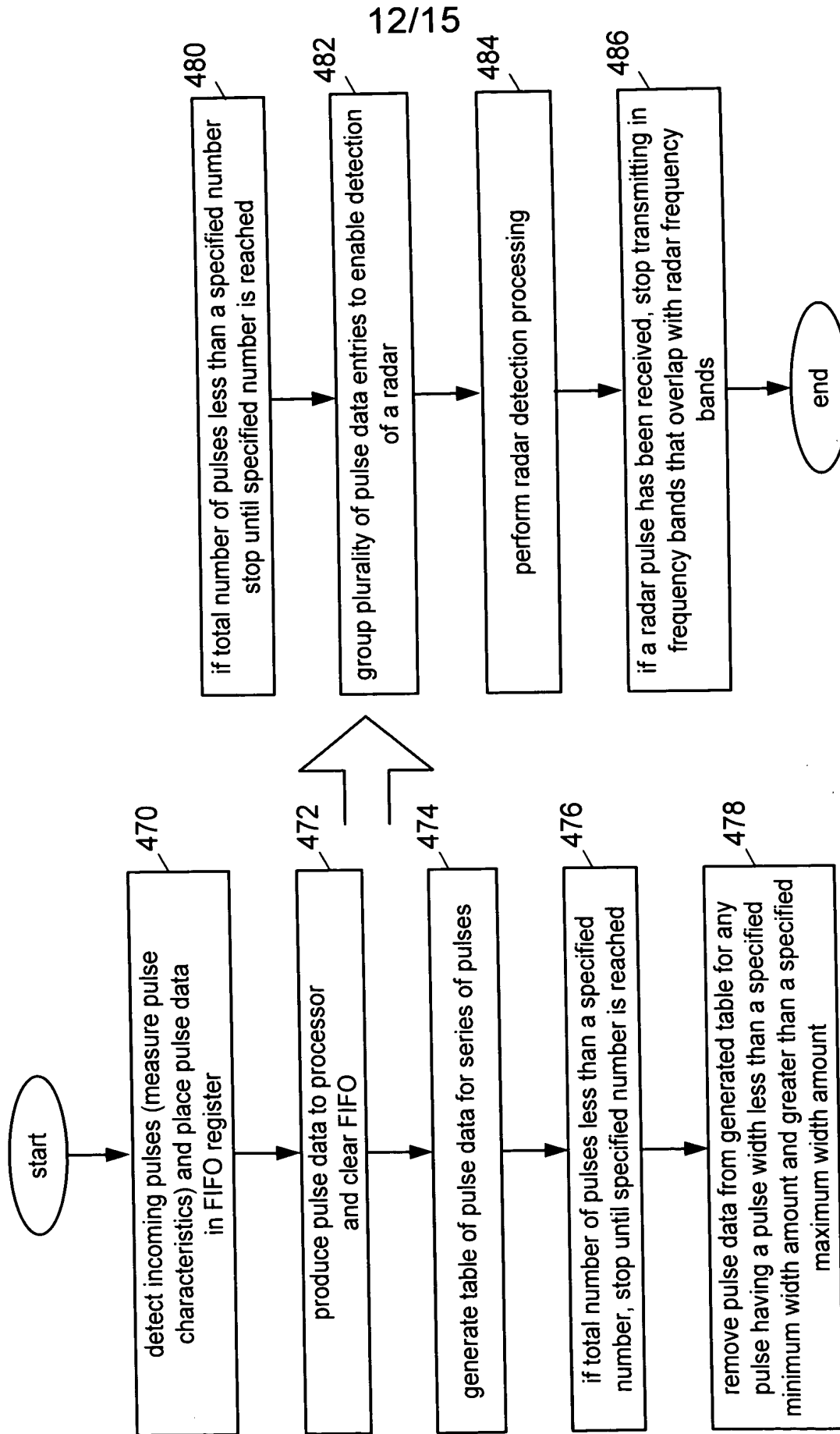
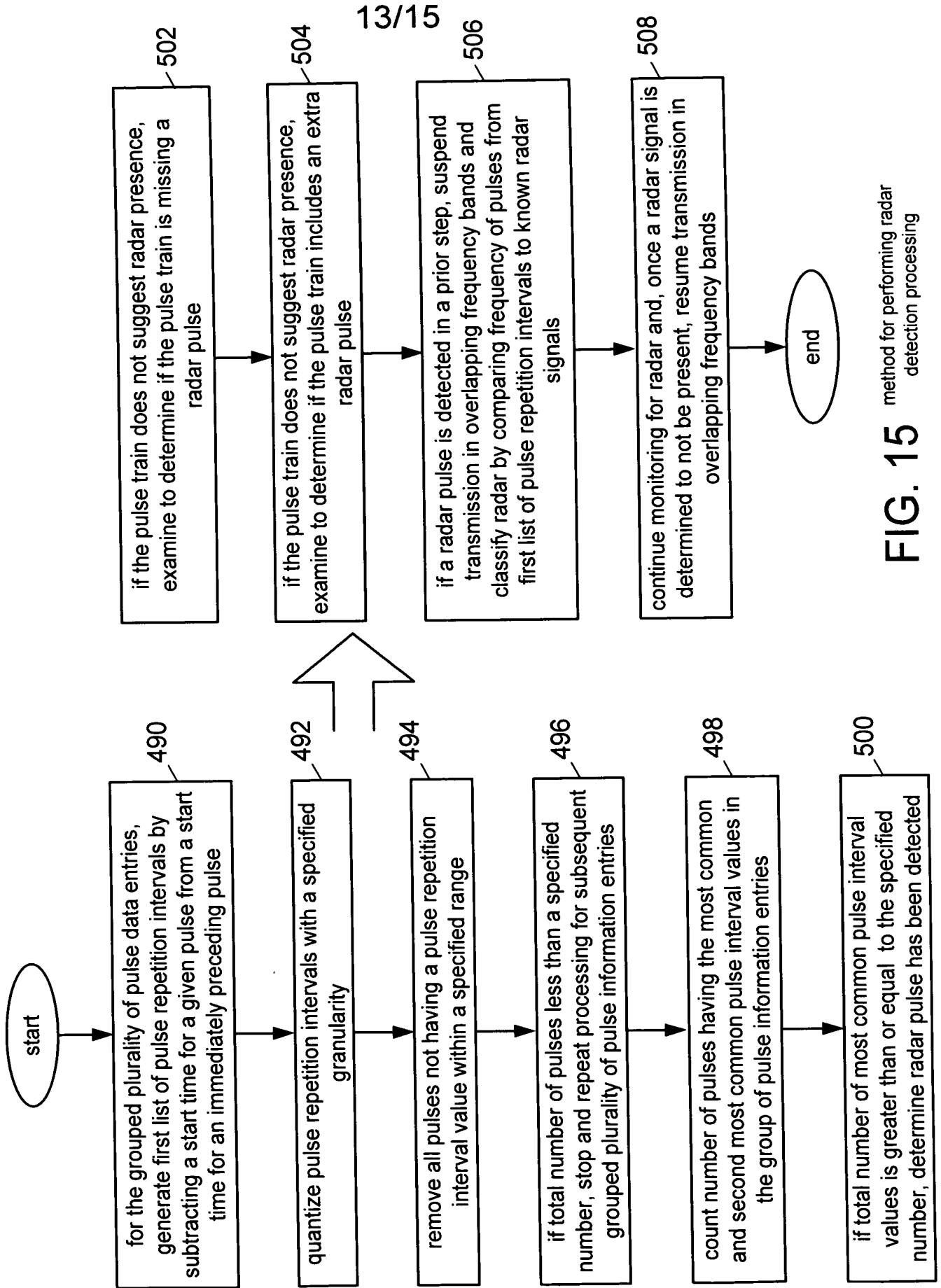


FIG. 13 method for determining if a radar pulse has been received

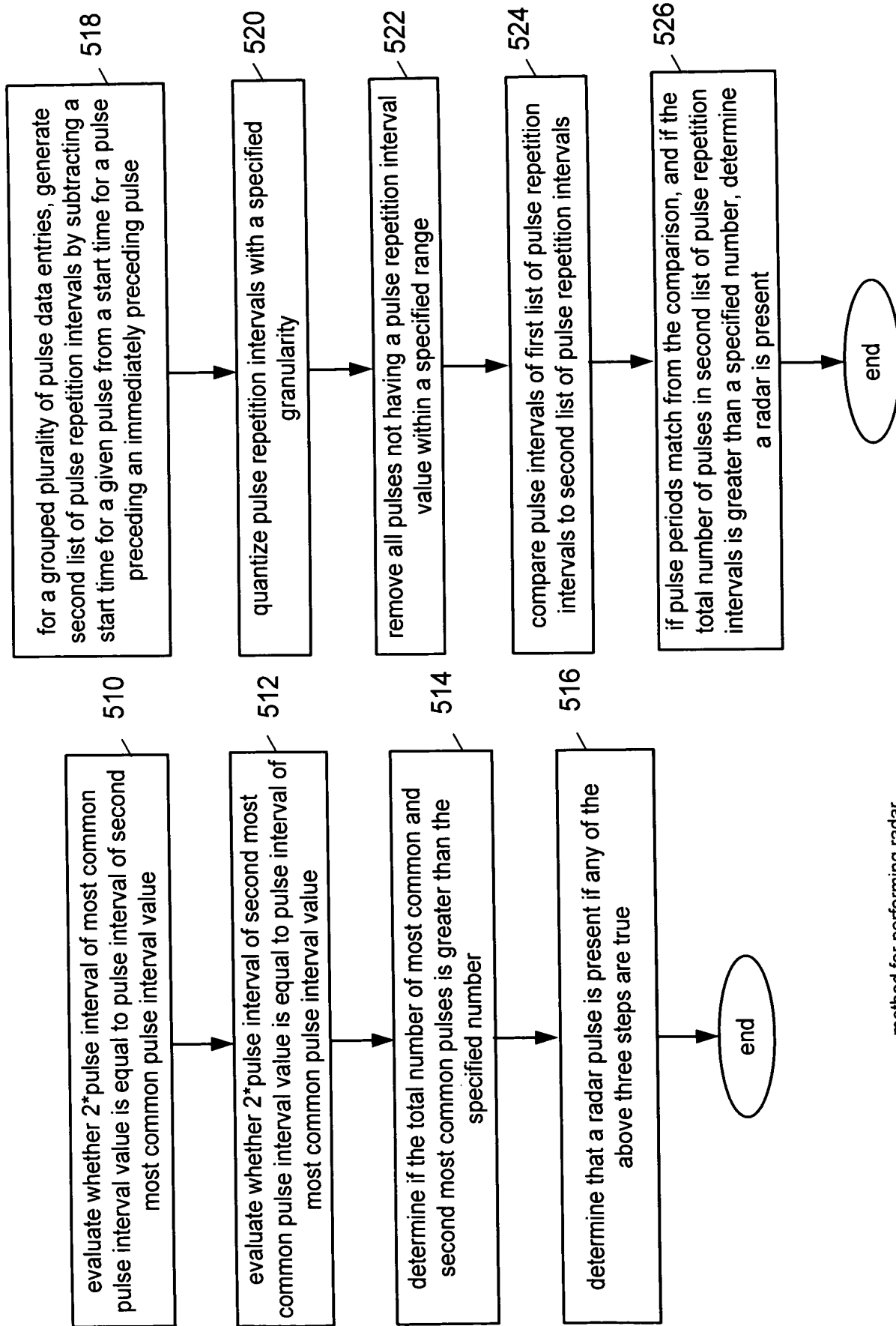


method for performing radar
detection processing

FIG. 14



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	start time	rise time	pulse width	fall time
pulse 1	A	B	C	D
pulse 2	E	F	G	H
pulse 3	I	J	K	L
pulse 4	M	N	O	P
pulse 5	Q	R	S	U
pulse n	W	X	Y	Z

method for performing radar
detection processing

FIG. 18

pulse interval	pulse interval quantity by type	pulse interval	pulse interval quantity by type
E - A	n1 E - A	I - A	n1 I - A
I - E	n2 I - E	M - E	n2 M - E
M - I	n3 M - I	Q - I	n3 Q - I
Q - M	n4 Q - M	W - M	n4 W - M
W - Q	n5 W - Q		

method for performing radar
detection processing for extra pulses

method for performing radar
detection processing for
missing pulses

FIG. 19

FIG. 20